



SWARNANDHRA
COLLEGE OF ENGINEERING & TECHNOLOGY
(Autonomous)
Seetarapuram, Narsapur-534280, W.G-DT, AP.

BOARD OF STUDIES (U.G)
Department of Electricals & Electronics Engineering

Sl. No	Name of the Member	Designation & Affiliation	Category
1	Dr.V.Moorthy	Professor & Head Dept. of EEE, SCET, Narsapur	Chairman
2	Dr.J.Mahil	Professor Dept. of EEE, SCET, Narsapur	Internal Member
3	Dr.C.Sivapragash	Professor Dept. of EEE, SCET, Narsapur	Internal Member
4	Mr.A.Satyanarayana	Assist. Professor Dept. of EEE, SCET, Narsapur	Internal Member
5	Mrs.U.N. Hymavathi	Assist. Professor, Dept. of EEE, SCET, Narsapur	Internal Member
6	Dr.S.Sivanagaraju	Professor Dept. of EEE, University College of Engineering JNTUK, Kakinada	Subject Expert Nominated by University
7	Dr.P.Sangameswararaju	Professor (Rtd) Dept. of EEE, S.V. University College of Engineering, Tirupati.	Subject Expert Nominated by Academic council
8	Dr. G. Venkata Siva Krishna Rao	Professor, Dept. of EEE, Andhra University Visakhapatnam, Andhra Pradesh, India	Subject Expert Nominated by Academic council
9	Er.N.Lakshman Rao	Assistant Engineer VTPS, Ibrahim Patnam.	Industrial Expert
10	Mr.V.Surya Manoj	Assist. Professor Dept. of EEE Vishnu Institute of Technology, Bhimavaram	Alumni Member


HEAD OF THE DEPARTMENT


PRINCIPAL



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(Autonomous)
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BOARD OF STUDIES (P.G)
Department of Electricals & Electronics Engineering

Sl. No	Name of the Member	Designation & Affiliation	Category
1	Dr.C.Sivapragash	Professor Dept. of EEE, SCET, Narsapur	Chairman
2	Dr.V.Moorthy	Professor & Head Dept. of EEE, SCET, Narsapur	Internal Member
3	Dr.J.Mahil	Professor Dept. of EEE, SCET, Narsapur	Internal Member
4	Mr.K.Suresh	Assist. Professor, Dept. of EEE, SCET, Narsapur	Internal Member
5	Mr.D.Nageswara Rao	Assist. Professor Dept. of EEE, SCET, Narsapur	Internal Member
6	Dr.S.Sivanagaraju	Professor Dept. of EEE, University College of Engineering JNTUK, Kakinada	Subject Expert Nominated by University
7	Dr.P.Sangameswararaju	Professor (Rtd) Dept. of EEE, S.V. University College of Engineering, Tirupati.	Subject Expert Nominated by Academic council
8	Dr. G. Venkata Siva Krishna Rao	Professor, Dept. of EEE, Andhra University Visakhapatnam, Andhra Pradesh, India	Subject Expert Nominated by Academic council
9	Er.N.Lakshman Rao	Assistant Engineer VTPS, Ibrahim Patnam.	Industrial Expert
10	Mr.V.Surya Manoj	Assist. Professor Dept. of EEE Vishnu Institute of Technology, Bhimavaram	Alumni Member


HEAD OF THE DEPARTMENT


PRINCIPAL

BOARD OF STUDIES MEETING FOR B.TECH COURSE

Department: Electrical and Electronics Engineering

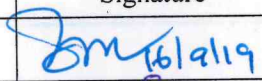
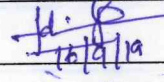
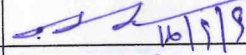
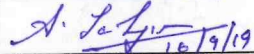
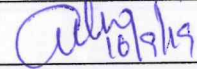


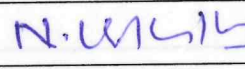
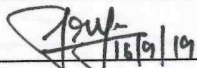
Venue: System Simulation Lab

Date and Time: 16th September 2019 at 2.00PM

Agenda:

1. Introduction of new members in BOS
2. Finalization of I Semester to VIII Semester course structure (Choice Based Credit System).
3. Finalization of I semester to IV semester syllabus
4. Any other matter with the permission of Chair.

Member attended for BOS Meeting

Sl. No	Name of the Member	Category	Signature
1	Dr.V.Moorthy	Chairman	 16/9/19
2	Dr.J.Mahil	Internal Member	 16/9/19
3	Dr.C.Sivapragash	Internal Member	 16/9/19
4	Mr.A.Satyanarayana	Internal Member	 16/9/19
5	Mrs.U.N. Hymavathi	Internal Member	 16/9/19
6	Dr.S.Sivanagaraju	Subject Expert Nominated by University	 16/9/19
7	Dr.P.Sangameswararaju	Subject Expert Nominated by A.C	 16/9/19
8	Dr. G. Venkata Siva Krishna Rao	Subject Expert Nominated by A.C	
9	Er.N.Lakshman Rao	Industrial Expert	 16/09/19
10	Mr.V.Surya Manoj	Alumni Member	 16/9/19

BOARD OF STUDIES MEETING FOR M.TECH COURSE

Department: Electrical and Electronics Engineering

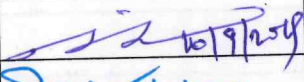
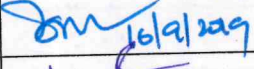
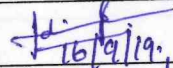
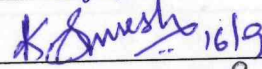
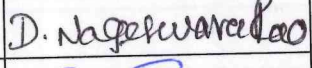
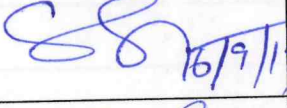
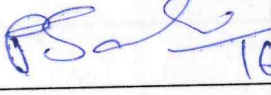
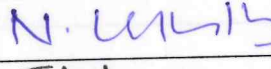
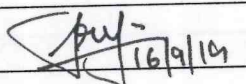
Venue: System Simulation Lab

Date and Time: 16th September 2019 at 2.00PM

Agenda:

1. Introduction of new members in BOS
2. Finalization of I Semester to IV Semester course structure (Choice Based Credit system).
3. Finalization of I semester to II semester syllabus
4. Any other matter with the permission of Chair.

Member attended for BOS Meeting

Sl. No	Name of the Member	Category	Signature
1	Dr.C.Sivapragash	Chairman	 16/9/2019
2	Dr.V.Moorthy	Internal Member	 16/9/2019
3	Dr.J.Mahil	Internal Member	 16/9/19.
4	Mr.K.Suresh	Internal Member	 K.Suresh 16/9
5	Mr.D.Nageswara Rao	Internal Member	 D. Nageswara Rao.
6	Dr.S.Sivanagaraju	Subject Expert Nominated by University	 16/9/19
7	Dr.P.Sangameswararaju	Subject Expert Nominated by A.C	 16/9/19
8	Dr. G. Venkata Siva Krishna Rao	Subject Expert Nominated by A.C	-
9	Er.N.Lakshman Rao	Industrial Expert	 N. Lakshman Rao 16/09/19
10	Mr.V.Surya Manoj	Alumni Member	 16/9/19

Minutes of Board of Studies (UG):

Dr. G.V. SivaKrishna Rao, Professor, Dept. of EEE, Andhra University has granted leave of absence.

The Chairman of BOS, Department of Electrical and Electronics Engineering has welcomed all members and briefed the agenda that have been deliberately considered for discussion.

Agenda-1: Introduction of new members in BOS

The BOS of EEE department is constituted as per the guidelines of the Academic Council and Vice-Chancellor of Jawaharlal Nehru Technology University, Kakinada. The Chairman has introduced the members.

Agenda-2: Finalization of the course structure from Semester I to Semester VIII (Choice Based Credit System).

The Chairman has presented the course structure which was drafted by syllabus frame-work committee, and the following resolutions were taken in consultation with the interdisciplinary members.

- It is observed from the past three batches of admission that the department student's strength depends on lateral entry. If the course OOP's through C++ presents in semester II, the students who will join in semester III (lateral entry students) might forego this course. Therefore, both theory and laboratory have transferred into semester III.
- In order to balance the credit in semester III, the course Fluid Mechanics and Hydraulic Machinery is removed. At the same time, a new course Basics of Mechanical Engineering is introduced in semester II which covers basics of the strength of material, thermodynamics and hydraulic machinery. The lateral entry students have studied these contents in their Diploma level. Additionally, Mechanical Engineering Laboratory is included in semester II.
- Electrical Networks in semester II and Electrical Circuits in semester III gives similar meaning though contents are different. As the Electrical

Networks is common for EEE and ECE, the subject Electrical Circuits is suitably renamed as Electrical Circuits and Synthesis.

- In the drafted course structure Electrical and Electronics Measurement have come in semester VII. It is decided to bring in an earlier semester either semester IV or semester V to facilitate the knowledge about meters and measurement techniques to the students. Therefore, it is included in semester V.
- It is decided to compress analog and digital electronics, Linear IC application into three-course rather than two. Therefore, Analog Electronics in semester III with Laboratory, Digital Electronics in semester IV with Laboratory and Linear IC Application in semester V.
- It is decided to teach Java and Python as a laboratory component with one lecture hour.

Finally, the BOS has finalized the new course structure for R19 regulation unanimously.

Agenda-3: Finalization of the syllabus from semester I to semester IV.

The BOS has deeply analyzed the drafted curriculum for R19 regulation, the course content of all subjects from semester I to semester IV have appropriately framed.

The semester wise changes are:

I semester:

For Mechanical Engineering (ME) and Civil Engineering (CE) branches, a course Basic Electrical Engineering is included in this semester. It addresses the basic concepts of electrical engineering with five units that provide DC and AC circuits, DC machines and single-phase Transformer, AC machines (3 phase induction motor and alternator only) and Electrical installation. By considering this course as co-requisites a laboratory is introduced with thirteen experiments, and these are grouped in correlation with theory course.

II semester:

For CSE and IT branches, the course Basic Electrical Engineering is included in this semester. The contents are remaining the same as ME and CE branches, but the course code is different.

In Electrical Networks, the contents are rearranged such that the fundamental of Electrical circuits, Network topology, Single phase AC circuits with resonance, Network theorems with DC and AC excitation and Magnetic circuits.

III Semester:

The course contents of Electrical Circuits and Synthesis reevaluated as follows: balanced and unbalanced three-phase circuits, transient analysis in DC and AC circuits using differential equation and Laplace transformation, two-port networks, and network synthesis. It is decided that the Fourier Analysis and Transforms to be included in the course Signals and System.

The course content of Electrical Engineering for ECE branch has restructured as follows: Network synthesis, DC machines, Transformers, three-phase AC system and Induction motor, and synchronous and special machines.

The course contents of Electrical Machines-I are decided with five units in which DC machines and single-phase Transformer have comprised two units each, and three-phase Transformers have discussed in one unit.

The course contents of Electromagnetic Fields are a static electric field, conductors, dielectrics and capacitance, static magnetic fields, magnetic forces, materials and inductance, and time-varying fields and Maxwell's equations. Notably, the subheadings are rearranged as per the textbook.

The list of experiments of Electrical Circuits and Simulation Laboratory are grouped as per the units and also decided to verify the experiments using any simulation tool.

IV Semester:

In the Electrical Machines-II course of earlier regulation, the unit content design of three-phase machines is deleted, and remaining contents are rearranged based on JNTUK, NITAP and NITW syllabus. The revised contents are Three-phase induction motor which is addressed in two units whereas, synchronous machines have discussed in next two units. Finally, single-phase induction motors have dealt in last unit.

The Control System course is common for EEE and ECE branches. The contents are distributed into five units considering GATE syllabus. These are

mathematical modeling of control systems, time response analysis, frequency response analysis and compensation, stability analysis, and state-space analysis.

The experiments in Electrical Machines-I laboratory are grouped as per the theory contents. Additionally, three experiments have included; these are brake test on DC series motor, parallel operation of single-phase transformer and separation of core losses of a single-phase transformer.

The Experiments in Control system and simulation lab are arranged in correlation with theory course.

Agenda-3: Any other matter with the permission of Chair.

Additionally, the following points are discussed/ensured:

- Pre-requisite and co-requisite are cross-checked.
- The industry expert has suggested that some additional experiment be conducted beyond the syllabus.
- Steps necessary to implement mini-project, mandatory course (Certification and MOOC's) have discussed.

The meeting ended with the vote of thanks.

Sitarampuram

16/09/2019


Chairman

CHAIRMAN
BOARD OF STUDIES
Department of EEE
Swarnandhra College of Engineering & Technology

Minutes of Board of Studies (PG):

Dr. G.V. SivaKrishna Rao, Professor, Dept. of EEE, Andhra University has granted leave of absence.

The Chairman of BOS, Department of Electrical and Electronics Engineering, has welcomed all members and briefed the agenda that have been deliberately considered for discussion.

Agenda-1: Introduction of new members in BOS

The BOS of EEE department is constituted as per the guidelines of the Academic Council and Vice-Chancellor of Jawaharlal Nehru Technology University, Kakinada. The Chairman has introduced the members.

Agenda-2: Finalization of course structure from semester I to semester IV (Choice Based Credit System).

The Chairman has presented the course structure which was drafted by syllabus frame-work committee, which is framed as per the AICTE model curriculum for Postgraduate Degree Courses in Engineering & Technology, January 2018. The BOS has unanimously finalized the new course structure for R19 regulation.

Agenda-3: Finalization of semesters I to II syllabus

The BOS has deeply analyzed the drafted curriculum for R19 regulation and the course content of all subjects from semester I to semester II have appropriately framed.

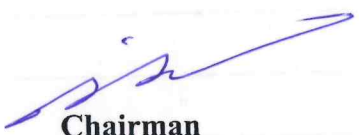
Agenda-3: Any other matter with the permission of Chair.

Additionally, guidelines necessary to write a technical seminar report has discussed.

The meeting ended with the vote of thanks.

Sitarampuram

16/09/2019


Chairman
CHAIRMAN
BOARD OF STUDIES
Department of EEE
Swarnandhra College of Engineering & Technology