

COLLEGE OF ENGINEERING & TECHNOLOGY

(AUTONOMOUS)

Accredited by National Board of Accreditation, AICTE, New Delhi, Accredited by NAAC with "A" Grade – 3.32 CGPA Recognized under 2(f) & 12(B) of UGC Act 1956, Approved by AICTE, New Delhi, Permanent Affiliation to JNTUK, Kakinada Seetharampuram, W.G.DT., Narsapur-534280, (Andhra Pradesh)

DEPARTMENT OF INFORMATION TECHNOLOGY TEACHING PLAN

Course Code		Course Title		Semester	Branch	Contact Periods /Week	Acad	lemic ear	Date of commencemen of Semester
23CS4T01		Operating Systems		IV	IT	5	202	4-25	16-12-2024
COUR	SE OUTC	OMES					-		
1	Define th	e Basic c	oncepts abo	out Operatin	g System ar	nd its fur	nctions. (K1, K2)	
2	77.00			t and CPU s	- Contract	Marie			
3	E. De Canada Proposition	- Principle Extended and	MATERIAL PARTITION	s, condition	MATERIAL PRINCIPAL PRINCIP	100111111111111111111111111111111111111	heir poss	sible solu	tions (K3)
4				nt and virtua					
5		THE PARTY OF THE P		otection tech		And the second	dem but	B. (-
2	Out	it The sy	stems & pro	Acction teen	inques. (K	1)			
UNIT	Comes / Bloom's Level	Topics No.		Topics. Activity			Text Book/ eference	Contact	Delivery Method
		1.1	Operating	Systems Ov	verview		T1,T2	1	
		1.2	Introducti	on			T1,T2	1	
		1.3	Operating	system fund	ctions		T1,T2	1	
		1.4	Operating	systems ope	erations		T1,T2	i i	1
		1.5	Computin	g environme	ents		T1,T2	1	Chalk
		1.6	Free and (Systems	Open-Source	e Operating		T1,T2	1	& Board
		1.7	System St System Se	ructures: Op ervices	erating		T1,T2	1	Power poin
I	CO – 1	1.8	User and Interface	Operating-S	ystem		T1,T2	1	presentation
		1.9	System ca Types of	ılls System Call:	s		T1,T2	1	Assignmen
		1.10	System pr	rograms			T1,T2	1	Test
		1.11	Operating Implemen	system Des tation	sign and		T1,T2	ī	
		1.12	- T	system stru			T1,T2	1	
		1.13	Building a System	and Booting	an Operati	ng	T1,T2	1	M. J.
		1.14	Operating	system deb	ugging		T1,T2	1	1
							Total	14	0.00
П	CO-2	2.1	Process M Introducti	fanagement on	System -		T1,R1	1	
	200	2.2	Process C	oncepts			T1,R1	1	



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25	-D.C. E. E.	2.3	Process scheduling	T2,R2	1 "	Chalk
		2.4	Operations on processes	T2,R2	1	& Board
		2.5	Inter-process communication	T2,R2	1	Doard
		2.6	Threads and Concurrency	T2,R2	1	
		2.7	Multithreading models	T2,R2	1	Power poin
		2.8	Thread libraries, Threading issues	T1,T2	1	presentation
		2.9	CPU Scheduling: Basic concepts	T1,T2	1	Assignmen
		2.10	Scheduling criteria	T1,T2	1	Assignmen
	3	2.11	Scheduling algorithms- FCFS, SJF	T1,T2	1	Test
		2.12	Scheduling algorithms- Priority Scheduling & Round-Robin	T1,T2	1	-7/1/4/109
		2.13	Multiple processor scheduling	T1,T2	1 *	
- A			I manage the second and the second a	Total	13	
		3.1	Synchronization Tools – Basic concepts	T1,T2	1	
		3.2	The Critical Section Problem	T1,T2	1 "	
		3.3	Peterson's Solution	T1,T2	1 *	Chalk
		3.4	Mutex Locks	T1,T2	1	&
	CO – 3	3.5	Semaphores	T1,T2	1	Board Power poir presentatio
		3.6	Monitors	T1,T2	1	
		3.7	Classic problems of Synchronization	T1,T2	1	
Ш		3.8	Deadlocks - Introduction	T1,T2	1	presentation
		3.9	System Model	T1,T2	1	Assignme
		3.10	Deadlock characterization	T1,T2	1	
		3.11	Methods for handling Deadlocks	T1,T2	1 *	Test
		3.12	Deadlock prevention	T1,T2	1 *	
		3.13	Deadlock avoidance	T1,T2	1	
		3.14	Deadlock detection	T1,T2	1.	
		3.15	Recovery from Deadlock	T1,T2	1	
	nt beyond Ilabus	3.16	Concurrency Control and Deadlock	T1,T2	1	
				Total	16	
IV	CO-4	4.1	Memory-Management Strategies: Introduction	T1,T2	1	Chalk &
		4.2	Contiguous memory allocation	T1,T2	1	Board Power poin presentation Assignmen
		4.3	Paging	T1,T2	1 3	
		4.4	Structure of the Page Table	T1,T2	1	
		4.5	Swapping	T1,T2	1 5	
		4.6	Virtual Memory Management: Introduction	T1,T2	1	
		4.7	Demand paging, Copy-on-write	T1,T2	1 4	Test
		4.8	Page replacement algorithm: First In	T1,T2		



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			First Out (FIFO)		9		
		4.9	Least Recently Used (LRU) and Optimal algorithms	T1,T2	1		
		4.10	Allocation of frames, Thrashing	T1,T2	1		
		4.11	Storage Management: Overview of Mass Storage Structure	T1,T2	1		
		4.12	HDD Scheduling	T1,T2	1		
				Total	12		
		5.1	File System: File System Interface: File concept	T1,R1	1		
		5.2	Access methods	T1,R1	1		
		5.3	Directory Structure	T1,R1	1	The state of the s	
		5.4	File system Implementation: File- system structure	T1,R1	1	Chalk &	
		5.5	File-system Operations	T1,R1	1	Board	
		5.6	Directory implementation	T1,R1	1 "		
V	CO - 5	5.7	Allocation method, Free space management	T1,R1	1	Power point presentation	
		5.8	File-System Internals: File- System Mounting	T1,R1	1	Assignmer Test	
		5.9	Partitions and Mounting, File Sharing	T1,R1	1		
		5.10	Protection: Goals of protection	T1,R1	1		
		5.11	Principles of protection, Protection Rings	T1,R1	1		
		5.12	Domain of protection, Access matrix	T1,R1	1 1		
				Total	12		
30.5			CUMULATIVE PROPOSED	PERIODS	67		
Text B	ooks:			"		211	
S. N	0. A	UTHORS,	BOOK TITLE, EDITION, PUBLISHER,	YEAR OF P	UBLICA	TION	
		Abraham Silberchatz, Peter B. Galvin, Greg Gagne, Operating System Concepts, 10 th Edition, John Wiley, 2018.					
2	Ta	anenbaum	A S, Modern Operating Systems, 4th Ed	ition, Pearson	n, 2016		
Refere	nce Boo	ks:			-		
S.N	0. A	UTHORS,	BOOK TITLE, EDITION, PUBLISHER,	YEAR OF P	UBLICA	TION	
1	E	ducation, 20					
2	D.M Dhamdhere, Operating Systems: A Concept Based Approach, 3 rd Edition, McGraw-Hill, 2013.						



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Web Det	ails:	
1	https://nptel.ac.in/courses/106/106/106106144/	
2	http://peterindia.net/OperatingSystems.html	
3	https://www.javatpoint.com/operating-system	
4	https://codex.cs.yale.edu/avi/os-book/OS10/practice-exercises/index-solu.html	
5	https://www.tutorialspoint.com/operating_system/os_overview.htm	

		Name	Signature with Date
i.	Faculty	Mr. Ch Rama Krishna Raju	for 16 holes
ii.	Module Coordinator	Mr. Ch Rama Krishna Raju	Lat minim
iii.	Programme Coordinator	Dr. RVVSV Prasad	RioSpicy 2

Principal