

SWARNANDHRA COLLEGE OF ENGINEERING AND TECHNOLOGY

(Autonomous)

Narsapur, West Godavari District, A.P. 534280

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

DEPARTMENT OFELECTRONICS AND COMMUNICATION ENGINEERING <u>TEACHING PLAN</u>

Course Code	CourseTitle	Semester	Branch	Contact Period /Week	Academic Year	Semester Commence ment Date		
20EC7E06	EMBEDDEDSYSTEMS (R-20)	VII HOH		2024-25	05-06-2024			
COURSE O				<u> </u>				
F	After completion of the cour		The state of the s					
COI	Demonstrate the fundament	als of Embed	lded Syster	ms (K3)	- 17			
CO2	Discriminate various components used in Embedded systems (K4)							
CO3	Analyze the PIC, AVR cont	rollers and P	rocessors(K4)				
CO4	Develop the case study of I	Embedded Sy	stems(K3)					

Unit No	Out Come/Bloom's Level		. Topics/Activity	Reference Text book	Contact Periods	Deli very Met hod
			UNIT-1			
	± 4 = 1	1.1	Introduction to Embedded Systems	T1,T2,R1	- 1	+
		1.2	Definition of Embedded System	T1,T2,R1	1	
		1.3	Embedded Systems Vs. General Computing Systems	T1,T2,R1	1	Chal
•		1.4	History of Embedded Systems,	T1,T2,R1	1	k&
Demonstrate fundamentals Embedded Systems (K.)		1.5	Classification of Embedded Systems	T1,T2,R1	1	Talk , PPT
		1.6	Major Application Areas	T1,T2,R1	1	
		1.7	Purpose of Embedded Systems	T1,T2,R1	1	* PP1
	Systems (K3)	1.8	Characteristics of Embedded Systems	T1,T2,R1	1	Tuto
		1.9	Operational Quality Attributes of Embedded Systems,	T1,T2,R1	1	rial.
		1.10	Non-Operational Quality Attributes of Embedded Systems	T1,T2,R1	1	
			Class Test		1	4
		-	P	TOTAL	11	
various components use			UNIT-II		1	
	CO-2:Discriminate	2.1	Core of the Embedded System:	T1,T2,R1	1	
	components used	2.2	General Purpose and Domain Specific Processors,	T1,T2,R1	1	
•	in Embedded	2.3	Microprocessors	T1,T2,R1	1	
systems (K4)	systems (K4)	2.4	Microcontrollers	T1,T2,R1	1	



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1 10		2.5	Application Specific Processors	[TI TO DI]			
2		2.6	PLDs	T1,T2,R1 T1,T2,R1	1		
	_	2.7	ASICs	T1,T2,R1	1	_	
		2.8	ASIP	T1,T2,R1	1	-	
		2.9	Commercial Off-The-Shelf Components (COTS)	T1,T2,R1	1	-	
		2.10	Memories: ROMs RAM	T1,T2,R1	1	Cha	
		2.11	Memory: RAMs	T1,T2,R1	1	k &	
	_ x	2.12	Memory according to the type of Interface	T1,T2,R1	1	Tall	
		2.13	Memory Shadowing	T1,T2,R1	- 1	,	
		2.14	Memory selection for Embedded Systems	T1,T2,R1	1	PP7	
Max.		2.15	Sensors	•			
		2.16	Actuators	T1,T2,R1	1	Tut	
		2.17	Onboard Communication Interfaces	T1,T2,R1	1	rial	
	2	2.18	External Communication Interfaces	T1,T2,R1	1		
			Class Test		1		
				TOTAL	19	1	
Jan. 1	*		UNIT-III				
		3.1	Embedded Firmware	T2,R1	1		
		3.2	Reset Circuit,	T2,R1	1		
	CO-1:Demonstrate	3.3	Brown-out Protection Circuit,	T2,R1	1		
	the basic	3.4	Oscillator Unit,	T2,R1	1	Cha	
3	knowledge about	3.5	Real Time Clock, Watchdog Timer,	T2,R1	1	k & Talk	
	fundamentals of	3.6	Embedded Firmware Design	T2,R1	1	,	
	Embedded	3.7	Approaches and Development Languages.	•T2,R1	1	PPT	
4 79	Systems (K3)	3.8	EXAMPLE PROGRAMS	T2,R1	1	&	
0			Class Test		1	Tuto	
		91		TOTAL	9	rial	
			UNIT-IV	TODI	1	4	
	CO-3: Analyze the PIC, AVR controllers and Processors(K4)	4.1	Overview of PIC,	T2,R1	1	-	
		4.2	Introduction to AVR controllers	T2,R1	1		
		4.3	Explanation on AVR controllers	T2,R1	1	Chal	
		4.4	Introduction to ARM processors:	T2,R1	1	k&	
		4.5	Explanation on ARM processors	T2,R1	1	Talk	
		4.6	Introduction to PIC family of Microcontroller,	T2,R1	1	PPT	
		4.7	Explanation on PIC family of Microcontroller,	T2,R1	1	8	
		4.8	Introduction to ARM family Processors,	T2,R1	1	Tuto	
		4.9	Explanation on ARM family Processors.	•T2,R1	1	rial	
4	1 0:	1.5	Class Test		1		



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97 VE				TOTAL	1	0	
			UNIT-V				
		5.1	Design Case studies: Digital clock,	T2,R1		1	
CO-4: Use design case study of	5.2	Design Case studies of Battery operated smartcard reader.			1		
	Embedded Systems(K3)	5.3	Design Case studies of Automated meter reading system	T2,R1		Ch	
5		5.4	Design Case studies of Digital camera	T2,R1		Ta	
	4	5.5	Design of Vending Machine	T2,R1	1	,	
	Content beyond Syllabus	5.6	Developing digital alarm	T2,R1	1	PP	
			Class Test		1	— &	
				TOTAL	7		
CONTE	NT BEYOND SYLLAI	BUS	•			7.77	
1.	Hardware software co-design techniques				1	Chalk &	
2.	Real time embedded Systems				245	Talk,	
				1	1	PPT & Tutorial	
1 d-			TOTAL PROPOSED NO. O	F CLASSES	58	Tutoriai	
Text Boo	oks:			8			
S.No.	AUTHORS/BOOK T	ITLE	/EDITION(latest)/PUBLISHER/YEAR OF PU	JBLICATION			
1	1. Shibu K.V, "Introduction to Embedded Systems", McGraw Hill, 2017 (Unit I-V)						
2	1. Raj Kamal, "Embe	edded	Systems", TMH.2018		×		
Referen	ce Books:						
S.No.	AUTHORS/BOOK T	ITLE/	EDITION(latest)/PUBLISHER/YEAR OF PU	BLICATION			
1	KENNETHAYYALA.	"The	8051 Microcontroller", Cingarelearning India 201	8			
2	David E Simon, "An Embedded Software Primer", Pearson Education, 2018.						
Web De	tails	-		-			
1	www.nptel.ac.in					\$.	
2	https://www.youtube.c	om/wa	tch?v=8-gUa7h5wzk&list=PLXnsjPD8-xuuIC5Gy	yfL8Iy6xysO2x4	leMt		
. 10			Name	Signature		4	
	Faculty		Mr.I.V.Ravi Kumar	0			
i.			Mr.I.V.Ravi Kumar	(3)			
i. ii.	Course Coordinator						
	Course Coordinator Module Coordinator		Dr.B.Ramana Kumar	my	•		

Principal Dr. S. Suresh Kumar