

SWARNANDHRA
COLLEGE OF ENGINEERING & TECHNOLOGY
AUTONOMOUS
EXAMINATION BRANCH

B.Tech. – III Semester (R16) REGULAR EXAMINATIONS, OCT/NOV - 2017

TIME TABLE

TIME : 02.00PM TO 05.00PM

| Branch | Date / Day | | | | |
|--|--|---|---|--|---|
| | 23-10-2017 MONDAY | 25-10-2017 WEDNESDAY | 27-10-2017 FRIDAY | 30-10-2017 MONDAY | 01-11-2017 WEDNESDAY |
| CIVIL ENGINEERING (CE – 01) | Strength of Materials (16CE3T01) | Fluid Mechanics (16CE3T02) | Surveying (16CE3T03) | Engineering Geology (16CE3T04) | Managerial Economics and Financial Analysis (16BM3T01) |
| ELECTRICAL & ELECTRONICS ENGINEERING (EEE – 02) | Electrical Circuit Analysis (16EE3T01) | Electronic Devices and Circuits (16EC3T02) | Fluid Mechanics and Hydraulic Machinery (16ME3T03) | Electromagnetic Fields (16EE3T02) | Electrical Machines-I (16EE3T03) |
| MECHANICAL ENGINEERING (ME – 03) | Thermo Dynamics (16ME3T01) | Mechanics of Solids (16ME3T02) | Fluid Mechanics and Hydraulic Machinery (16ME3T03) | Metallurgy and Material Science (16ME3T04) | Managerial Economics and Financial Analysis (16BM3T01) |
| ELECTRONICS & COMMUNICATION ENGINEERING (ECE – 04) | Signals and Systems (16EC3T01) | Electronic Devices and Circuits (16EC3T02) | Digital Electronics (16EC3T03) | Electrical Engineering (16EE3T04) | Complex Variables and Random Variables (16MA3T02) |
| COMPUTER SCIENCE AND ENGINEERING (CSE – 05) | Data Structures (16CS3T01) | Database Management Systems (16CS3T02) | Software Engineering (16CS3T03) | Digital Logic Design (16EC3T04) | Discrete Mathematics (16MA3T01) |
| INFORMATION TECHNOLOGY (IT – 12) | Data Structures (16CS3T01) | Operating Systems (16IT3T01) | Software Engineering (16CS3T03) | Computer Organization (16IT3T02) | Discrete Mathematics (16MA3T01) |

Date : 11-09-2017
Narsapur.

G.N.Visweswara Rao
Controller of Examinations

Dr. M. Sreenivasa Kumar
Principal